

UK

<b>Notice of Allowability</b>	Application No.	Applicant(s)	
	10/611,744	JANNING, JOHN L.	
	Examiner	Art Unit	
	Alan Cariaso	2875	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed February 11, 2005.
2. ☒ The allowed claim(s) is/are 3-5, 7-10, 13-15 and 17-21.
3. ☒ The drawings filed on 01 July 2003 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

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|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment                               |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|   | 9. <input type="checkbox"/> Other _____.   |

### **REASONS FOR ALLOWANCE**


1. The following is an examiner's statement of reasons for allowance: Claims 3-5, 7-10, 13-15 and 17-21 are allowable because none of the prior art of record discloses or suggests at least: (a) a shunt device that includes a first conductive member being a spring, connected at one end to one side of a first semiconductor chip (claims 3-5, 7 & 8); (b) a light socket including a housing having a pair of conductive terminals each having a plug-in socket surface formed therein which reside in the lower portion of the housing and a semiconductor chip having a first conductive lead connected to one side thereof and having a terminal end configured to plug into one of the plug-in socket surfaces and a second conductive lead connected to another side of the chip and having a terminal end configured to plug into another of the plug-in socket surfaces (claims 9 & 10); (c) a light socket that includes a first conductive member being a spring, connected at one end to one side of a first semiconductor chip (claims 13-15, 17, 18); (d) a shunt device which includes a bent conductive member having a first semiconductor chip connecting to one end and which is configured for the semiconductor chip to be inserted between the bent portion of each terminal and with another end of the bent member serving to retain the member and chip between the conductive terminals in the lower portion of the housing (claim 19); (e) a light socket that includes a housing having a pair of conductive terminals each terminal operatively connected to a wire leading outside the housing and having a terminal end within the lower portion which includes retention fingers which are opposing each other in a spaced relationship in a manner to form a retaining seat for the chip between the fingers (claims 20 & 21).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alan Cariaso whose telephone number is (571) 272-2366. The examiner can normally be reached on 9-5:30 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sandra O'Shea can be reached on (571) 272-2378. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Alan Cariaso  
Primary Examiner  
Art Unit 2875